

**PATENT APPLICATION** 

S. Hu

110791

Group Art Unit: 2811

Examiner:

Docket No.:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yasushi YAMAZAKI et al.

Application No.: 09/970,766

Filed: October 5, 2001

METHOD OF MANUFACTURING SEMICONDUCTOR SUBSTRATE,

SEMICONDUCTOR SUBSTRATE, ELECTRO-OPTICAL APPARATUS AND

**ELECTRONIC EQUIPMENT** 

## **SUPPLEMENTAL AMENDMENT**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

For:

Further to our June 19, 2003 personal interview, the period for reply being extended by the attached Petition for Extension of Time, please consider the following:

Amendments to the Drawings include an attached replacement sheet; and Remarks.

TECHNOLOGY CENTER 2801

RECEIVED